



TERRESTRIAL TRUNKED RADIO



Instruments

FREEDOM
Communication Technologies
R8100 Communication System Analyzer

FREEDOM
Communication Technologies
R8000C Communication System Analyzer

FREEDOM
Communication Technologies
R9000 Communication System Analyzer

Frequencies & Channels

TETRA in Europe:
380 to 400 MHz
410 to 430 MHz
450 to 470 MHz

TETRA in Asia:
350 to 380 MHz
806 to 821 MHz, 851 to 866 MHz
870 to 876 MHz, 915 to 921 MHz

Typical duplex spacing:
5 MHz or 10 MHz (at 300 to 500 MHz) or 45 MHz (at 800 to 1000 MHz)

Calculation of RF parameters:

DL carrier frequency = frequency band · 100 MHz + radio carrier number · 25 kHz + frequency offset

UL carrier frequency = DL carrier frequency – duplex offset

Example: TETRA band from 410 to 430 MHz, first UL channel = 410.0125 MHz, equivalent DL channel = 420.0125 MHz

Duplex spacing = 10 MHz Duplex offset = 12.5 kHz

Frequency band = 4 (400 MHz) Radio carrier number = 800

Bursts & Frames

Control uplink burst

34 ramping & PA linearis.	4 tail bits	84 scrambled bits	30 extd. training seq.	84 scrambled bits	4 tail bits	15 bits guard period
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Normal uplink burst

34 ramping & PA linearis.	4 tail bits	216 scrambled bits block 1	22 bits training seq.	216 scrambled bits block 2	4 tail bits	14 bits guard period
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Normal continuous downlink burst

12 train. seq.	2 phase adj.	216 scrambled bits block 1	14 bits broad. block	22 bits training sequence	16 bits broad. block	216 scrambled bits PA linearisation block 2	2 phase adj.	10 train. seq.
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Synchronisation continuous downlink burst

12 train. seq.	2 phase adj.	80 frequency correction	120 scrambled synchronisation bits block 1	38 synchron. training seq.	30 scr. bits broadcast block	216 scrambled bits PA linearisation block 2	2 phase adj.	10 train. seq.
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Measurements & Limits

RF power
Maximum power, power control steps; see table in previous column

Burst power versus time

$L_{min} = \max(-70 \text{ dBc}, -36 \text{ dBm})$

Burst type

Burst type	t_1	t_2	t_3
Control uplink burst	16 symbols	103 symbols	15 symbols
Normal uplink burst	16 symbols	231 symbols	15 symbols
Discontinuous downlink burst	7 symbols	246 symbols	7 symbols
Continuous downlink burst	Unspecified	Unspecified	Unspecified

Advantages of TETRA Technology

Fast call setup time (group call: < 300 ms)	Security features Emergency and priority calls
Individual and group calls	High spectral efficiency
Direct mode communication between radios	Infrastructure separate from public mobile networks (avoids congestion)
Data services	Fallback mode for base stations
Frequency-economic	

Modulation

Format: $\pi/4$ DQPSK (differential quadrature phase shift keying, shifted by 45°)

The phase change determines the information transferred.

Phase change	Transferred bits
$+\pi/4$	00
$+3\pi/4$	01
$-3\pi/4$	11
$-\pi/4$	10

General Technical Data

Channel bandwidth	25 kHz
Access technology	TDMA
Time slots (channels per carrier)	4
Modulation	$\pi/4$ DQPSK (2 bits per symbol)
Symbol rate	18 000 symbols/s (255 symbols/slot)
Maximum data rate	28.8 kbit/s
Call setup time	< 300 ms
Communication	Point to point (duplex, simplex) Point to multipoint
Encryption	Air interface End to end
Voice codec	ACELP (Algorithmic Code Excited Linear Prediction), 4.8 kbit/s

TEDS

TEDS (TETRA Release 2)	Extended air interface specification for higher data rates on traffic channels
Channel bandwidth	25 kHz (8 sub-carriers) 50 kHz (16 sub-carriers) 100 kHz (32 sub-carriers) 150 kHz (64 sub-carriers)
Access technology	TDMA/OFDMA
Time slots	4
Modulation	Quadrature Amplitude Modulation (QAM): 4-QAM, 16-QAM, 64-QAM
Symbol rate on each sub-carrier	2400 symbols/s (34 symbols/slot)
Downlink packet data throughput (kbit/s)	25 kHz 50 kHz 100 kHz 150 kHz
4-QAM	11 27 58 90
16-QAM	22 54 116 179
64-QAM	33 80 175 269
64-QAM	44 107 233 359
64-QAM	66 160 349 538

Power Levels, Power Control

Power class	Max. power level	Power class	Max. power level
1	30.0 W	45.0 dBm	V+D only
1L	17.5 W	42.5 dBm	
2	10.0 W	40.0 dBm	
2L	5.6 W	37.5 dBm	
3	3.0 W	35.0 dBm	DMO

Power step	Power class 1 (30 W)	Power class 2 (10 W)	Power class 3 (3 W)	Power class 4 (1W)
1 (45 dBm)	45 dBm ± 2 dB	40 dBm ± 2 dB	35 dBm ± 2 dB	30 dBm ± 2 dB
2 (40 dBm)	40 dBm ± 2.5 dB	40 dBm ± 2 dB	35 dBm ± 2.5 dB	30 dBm ± 2 dB
3 (35 dBm)	35 dBm ± 2.5 dB	35 dBm ± 2.5 dB	35 dBm ± 2 dB	30 dBm ± 2 dB
4 (30 dBm)	30 dBm ± 2.5 dB	30 dBm ± 2.5 dB	30 dBm ± 2.5 dB	30 dBm ± 2 dB
5 (25 dBm)	25 dBm ± 2.5 dB	25 dBm ± 2.5 dB	25 dBm ± 2.5 dB	25 dBm ± 2.5 dB
6 (20 dBm)	20 dBm ± 2.5 dB	20 dBm ± 2.5 dB	20 dBm ± 2.5 dB	20 dBm ± 2.5 dB
7 (15 dBm)	15 dBm ± 2.5 dB	15 dBm ± 2.5 dB	15 dBm ± 2.5 dB	15 dBm ± 2.5 dB

Power step	Power class 1L (17.5 W)	Power class 2L (5.6 W)	Power class 3L (1.8 W)	Power class 4L (0.56 W)
1 (45 dBm)	42.5 dBm ± 2 dB	37.5 dBm ± 2 dB	32.5 dBm ± 2 dB	27.5 dBm ± 2 dB
2 (40 dBm)	40 dBm ± 2.5 dB	37.5 dBm ± 2 dB	32.5 dBm ± 2 dB	27.5 dBm ± 2 dB
3 (35 dBm)	35 dBm ± 2.5 dB	35 dBm ± 2.5 dB	32.5 dBm ± 2 dB	27.5 dBm ± 2 dB
4 (30 dBm)	30 dBm ± 2.5 dB	30 dBm ± 2.5 dB	30 dBm ± 2.5 dB	27.5 dBm ± 2 dB
5 (25 dBm)	25 dBm ± 2.5 dB	25 dBm ± 2.5 dB	25 dBm ± 2.5 dB	25 dBm ± 2.5 dB
6 (20 dBm)	20 dBm ± 2.5 dB	20 dBm ± 2.5 dB	20 dBm ± 2.5 dB	20 dBm ± 2.5 dB
7 (15 dBm)	15 dBm ± 2.5 dB	15 dBm ± 2.5 dB	15 dBm ± 2.5 dB	15 dBm ± 2.5 dB

Residual carrier power

DC offset in the I-Q modulator
Limit = 5%

Vector error

Deviation of the measured vector from the ideal vector, relative to the magnitude of the ideal vector.
Peak vector error (within a burst) – limit: 30%
RMS vector error (averaged over a burst) – limit: 10%

Receiver measurements

Based on bit error rate (BER) measurements at a defined input power level

T1 signal: The test equipment transmits a pseudo-random bit sequence, the MS synchronises onto the signal and counts bit errors (measurement in the MS)

TT loopback: Receiver test mode initiated through a designated test protocol. The MS loops back the received bit sequence to the tester, the tester counts bit errors (measurement in the test equipment)

T1 loopback: Receiver test mode in which the MS loops back the received bit sequence to the tester without any protocol (no call being set up). The tester counts bit errors (measurement in the test equipment)

Limit: 0.01% at -112 dBm (receiver sensitivity, static conditions)

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